

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	1	((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity interconnect interconnection)) with (FPGA PGA PLA (programmable adj (array logic))) and lfsr	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/13 10:10
L9	13	((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity interconnect interconnection)) same (FPGA PGA PLA (programmable adj (array logic))) and LFSR	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/13 12:14
S1	50	("5416783" "5991909" "5280488" "5574733" "5617531" "5659557" "5680340" "5875200" "5938784" "5206824" "6138264" "6353842" "6539409" "6557129" "6708192" "6829740" "6874109" "6917218" "20020016806" "20030110193" "20030115521" "20030115525" "20040107233" "20040172431" "20040210810" "20050015688" "20050097419" "5258936" "5528607" "5790626" "5878057" "6189124" "6560727" "6763363" "6912683" "7028059" "20030061561" "20030236803" "20040091106" "20050097153" "20060156187" "20060168495" "6684358" "6950974" "7093175" "20040128599" "20040230884" "20070011530" "6105154" "5193092").pn.	US-PGPUB; USPAT	OR	ON	2007/03/05 14:55
S2	38896	(test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 21:29
S3	228829	FPGA PGA PLD PLC (programmable adj logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 18:30
S4	238587	FPGA PGA PLD PLC (programmable adj logic) (programmable near2 array)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 19:18

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S5	9681	LFSR (feedback near2 register) (pseudorandom near2 generator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 19:09
S6	9450	(FPGA PGA PLD PLC (programmable adj logic) (programmable near2 array)) and LFSR (feedback near2 register) (pseudorandom near2 generator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 19:18
S7	137	(test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity) and (FPGA PGA PLD PLC (programmable adj logic) (programmable near2 array)) and (LFSR (feedback near2 register) (pseudorandom near2 generator))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 19:20
S8	64	(test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity) and (FPGA PGA PLD PLC (programmable adj logic) (programmable near2 array)) and (LFSR (feedback near2 register) (pseudorandom near2 generator)) and (test near3 (pattern vector sequence))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 20:07
S9	1	S1 and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 19:22
S12	97	((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity)).ti.	US-PGPUB	OR	ON	2007/03/16 18:31
S13	2	"6161195".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/06 18:40
S14	2	"6988238".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/06 18:44
S15	2	"6807585".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/06 18:45
			US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB			

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S16	239978	FPGA PGA PLD PLC (programmable adj (array circuit device logic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 18:30
S17	9714	LFSR (feedback near2 register) (pseudorandom near2 generator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 20:15
S18	1210	S16 and S17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 18:30
S19	98	((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity)).ti.	US-PGPUB	OR	ON	2007/03/16 18:31
S20	0	S18 and S19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 18:31
S21	9498	((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity interconnect interconnection))	US-PGPUB	OR	ON	2007/08/13 06:06
S22	74	S16 and S17 and S21	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 18:40
S23	45	(716/4.ccls. 716/5.ccls. 716/6.ccls.) and ((gate gating) near2 rule)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 18:43
S24	17	"703"/\$.ccls. and ((gate gating) near2 rule)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 18:44
S25	51650	LFSR (feedback near2 register) ((random pattern pseudorandom) near2 generator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 20:15

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S26	153	S16 and S21 and S25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 20:27
S27	3	"2002220061"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 23:35
S28	47	"220061"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 23:35
S29	0	"20020220061"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 23:36
S30	1	"200220061"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/16 23:36
S31	32	((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity interconnect interconnection)) with (FPGA PGA PLA (programmable adj (array logic)))	US-PGPUB	OR	ON	2007/08/13 06:32
S32	3	((((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity interconnect interconnection)) with (FPGA PGA PLA (programmable adj (array logic)))) and LFSR	US-PGPUB	OR	ON	2007/08/13 06:08
S33	5	((((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity interconnect interconnection)) same (FPGA PGA PLA (programmable adj (array logic)))) and LFSR	US-PGPUB	OR	ON	2007/08/13 06:09
S34	110	((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity interconnect interconnection)) with (FPGA PGA PLA (programmable adj (array logic)))	US-PGPUB; USPAT	OR	ON	2007/08/13 06:34

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S35	28	((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity interconnect interconnection)) with (FPGA PGA PLA (programmable adj (array logic)))	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/13 10:10
S36	78	((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity interconnect interconnection)) with (FPGA PGA PLA (programmable adj (array logic)))	USPAT	OR	ON	2007/08/13 06:51
S37	1	(10/853981).APP.	USPAT; USOCR	OR	ON	2007/08/13 06:37
S38	157	((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity interconnect interconnection)) same (FPGA PGA PLA (programmable adj (array logic)))	USPAT	OR	ON	2007/08/13 12:13
S39	7	((test tester testing tested verify verification verifying verified) near2 (connection connect connecting connectivity interconnect interconnection)) same (FPGA PGA PLA (programmable adj (array logic))) and LFSR	USPAT	OR	ON	2007/08/13 12:13